

INTERNATIONAL IEEE Std 1800.2™ STANDARD

**SystemVerilog –
Part 2: Universal Verification Methodology Language Reference Manual**



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IEC Secretariat
3, rue de Varembe
CH-1211 Geneva 20
Switzerland
Tel.: +41 22 919 02 11
info@iec.ch
www.iec.ch

Institute of Electrical and Electronics Engineers, Inc.
3 Park Avenue
New York, NY 10016-5997
United States of America
stds.info@ieee.org
www.ieee.org

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