

INTERNATIONAL ELECTROTECHNICAL COMMISSION
COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

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**INTEGRATED CIRCUITS –
EMC EVALUATION OF TRANSCEIVERS –**

Part 3: CAN transceivers

**CIRCUITS INTÉGRÉS – ÉVALUATION DE LA
CEM DES ÉMETTEURS-RÉCEPTEURS –**

Partie 3: Émetteurs-récepteurs CAN

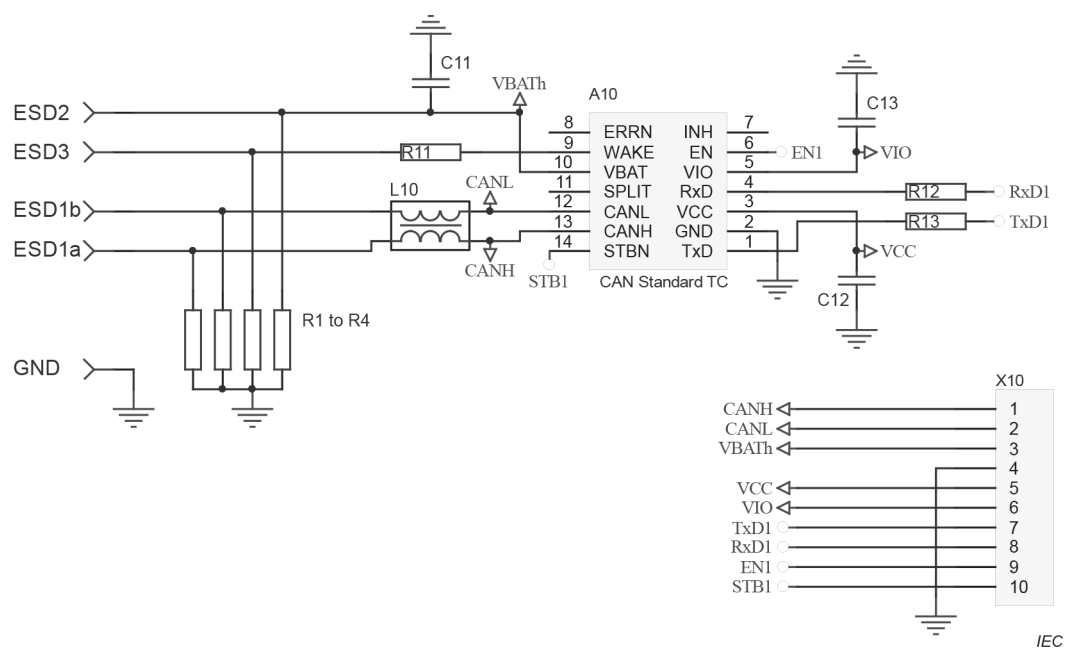
C O R R I G E N D U M 1

Corrections to the French version appear after the English text.

Les corrections à la version française sont données après le texte anglais.

A.3 Test circuit for CAN transceiver for ESD test

Replace the existing Figure A.3 with the following new Figure A.3:



Key

Components

A10	CAN standard transceiver
C11, C12, C13	capacitor $C = 100 \text{ nF}$
L10	common mode choke $L = 100 \text{ }\mu\text{H}$ (default value, placement depend on test case)
R1, R2, R3, R4	resistor $R \geq 200 \text{ k}\Omega$ (placement is optional)
R11	resistor $R = 33 \text{ k}\Omega$
R12, R13	resistor $R = 1 \text{ k}\Omega$
X10	connector to adaptation board

Figure A.3 – General drawing of the circuit diagram for direct ESD tests of CAN transceivers in unpowered mode