

Delay and power calculation standards —

Part 2: Pre-layout delay calculation specification for CMOS ASIC libraries

The European Standard EN 61523-2:2002 has the status of a
British Standard

ICS 35.240.50

National foreword

This British Standard is the official English language version of EN 61523-2:2002. It is identical with IEC 61523-2:2002.

The UK participation in its preparation was entrusted to Technical Committee GEL/93, Design automation, which has the responsibility to:

- aid enquirers to understand the text;
- present to the responsible European committee any enquiries on the interpretation, or proposals for change, and keep the UK interests informed;
- monitor related international and European developments and promulgate them in the UK.

A list of organizations represented on this committee can be obtained on request to its secretary.

Cross-references

The British Standards which implement international or European publications referred to in this document may be found in the *BSI Catalogue* under the section entitled “International Standards Correspondence Index”, or by using the “Search” facility of the *BSI Electronic Catalogue* or of British Standards Online

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This British Standard, having been prepared under the direction of the Electrotechnical Sector Policy and Strategy Committee, was published under the authority of the Standards Policy and Strategy Committee on 27 September 2002

Summary of pages

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