

Mechanical standardization of semiconductor devices —

**Part 6-5: General rules for the
preparation of outline drawings of
surface mounted semiconductor device
packages — Design guide for fine-pitch
ball grid array (FBGA)**

The European Standard EN 60191-6-5:2001 has the status of a
British Standard

ICS 31.080.01

National foreword

This British Standard is the official English language version of EN 60191-6-5:2001. It is identical with IEC 60191-6-5:2001.

The UK participation in its preparation was entrusted by Technical Committee EPL/47, Semiconductors, to Subcommittee EPL/47/4, Mechanical standardization, which has the responsibility to:

- aid enquirers to understand the text;
- present to the responsible European committee any enquiries on the interpretation, or proposals for change, and keep the UK interests informed;
- monitor related international and European developments and promulgate them in the UK.

A list of organizations represented on this Subcommittee can be obtained on request to its secretary.

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This British Standard, having been prepared under the direction of the Electrotechnical Sector Policy and Strategy Committee, was published under the authority of the Standards Policy and Strategy Committee on 15 November 2001

Summary of pages

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Amendments issued since publication

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