



BSI Standards Publication

Mechanical standardization of semiconductor devices

Part 6-20: General rules for the preparation
of outline drawings of surface mounted
semiconductor device packages —
Measuring methods for package
dimensions of small outline J-lead
packages (SOJ)

National foreword

This British Standard is the UK implementation of EN 60191-6-20:2010. It is identical to IEC 60191-6-20:2010.

The UK participation in its preparation was entrusted to Technical Committee EPL/47, Semiconductors.

A list of organizations represented on this committee can be obtained on request to its secretary.

This publication does not purport to include all the necessary provisions of a contract. Users are responsible for its correct application.

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Amendments issued since publication

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